

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1548	349/57-60.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 07:12
L2	1849	349/149-152.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 07:12
L3	162	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 07:12
L10	270	((pcb ((circuit wiring) adj board)) near5 (hole slit\$3 open\$3) with (frame cas\$3)) with (protru\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 07:32
L12	77	((pcb ((circuit wiring) adj board)) with (hole slit\$3 open\$3 aperture) with (frame cas\$3)) and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 07:36
L13	31	((pcb ((circuit wiring) adj board)) with (hole slit\$3 open\$3) with (frame cas\$3)) with (protru\$5 fix\$5 project\$5 pin boss) and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 07:46
L15	355	((pcb ((circuit wiring) adj board)) with (hole slit\$3 open\$3) with (frame cas\$3)) with (protru\$5 project\$5 pin boss) and display	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 08:27
L16	116	349/60.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 08:27
L21	7	"349"/\$3.ccls. and ((pcb ((circuit wiring) adj board)) with (hole slit\$3 open\$3 aperture) with (frame cas\$3)) with (protru\$5 fix\$5 project\$5 pin boss).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 08:48
L22	134	((pcb ((circuit wiring) adj board)) with (hole slit\$3 open\$3 aperture) with (frame cas\$3)) with (protru\$5 fix\$5 project\$5 pin boss).clm.	US-PGPUB	OR	ON	2005/11/28 08:48
S1	2	("5897188").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 08:58
S2	2	("6478622").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 08:58

S3	2	("5946195").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/06/09 09:01
S4	28	349/149,150.ccls. and ((pcb (circuit adj board)) with (hole slit\$3 open\$3) with (frame cas\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 08:46
S5	5	349/149,150.ccls. and ((pcb (circuit adj board)) with (press\$5 adj fit\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 09:57
S6	5	349/149,150.ccls. and ((pcb (circuit adj board)) with (snap\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 09:59
S7	6	349/149-152.ccls. and ((pcb (circuit adj board)) with (snap\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 09:59
S8	85	349/149,150.ccls. and ((pcb (circuit adj board)) with (hole slit\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 10:00
S9	2	"349"/\$3.ccls. and ((pcb (circuit adj board)) with (hole slit\$3 open\$5) with (oval\$5 ovul\$5 ellip\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 13:04
S10	184	((pcb (circuit adj board)) with (hole slit\$3 open\$5) with (oval\$5 ovul\$5 ellip\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 13:05
S11	127	((pcb (circuit adj board)) with (hole slit\$3 open\$5) with (oval\$5 ovul\$5 ellip\$6)) and (pcb (circuit adj board)).ti,ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 13:06
S12	60	((pcb (circuit adj board)) with (hole slit\$3 open\$5) with (oval\$5 ovul\$5 ellip\$6)).ti,ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 13:10
S13	184	((pcb (circuit adj board)) with (hole slit\$3 open\$5) with (oval\$5 ovul\$5 ellip\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 13:10
S14	14	(portable adj display) and ((matrix near5 array) with color) and "349"/\$3.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/09 13:33